

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	168	metal near5 (board or plate or base) with portion and pad and (first and second) near (insulat\$3 or dielectric or passivat\$3) and (power and control or first and second) near2 (chip or die or device or component)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/10/29 12:46
L5	57	4 and @pd< "20040415"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/10/29 12:47
L6	369	((361/748,750,761,764,783) or (257/678,701,708,723,787,E21.504,E23.004,E23.036,E23.066,E23.092) or (174/250,251,258,256,257,260,268)).ccls. and @pd> "20090728"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/10/29 12:50
L7	4	6 and (metal near (plate or strap or sheet) or print \$3 near2 board or PWB or PCB) near5 (groove or trench or recess or cavity or opening) near5 (insulat \$3 or resin or fill\$3) and (chip or die or device or component or element)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/10/29 12:50
L8	2	((TAKASHI) near2 (SAIKI)).INV. and @pd> "20090728"	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/10/29 12:51

L9	0	((HIDENORI) near2 (ISHIKURA)).INV. and @pd> "20090728"	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/10/29 12:51
L10	0	((YOSHIHIKO) near2 (TSUJIMURA)).INV. and @pd> "20090728"	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/10/29 12:51
L11	0	((KATUNORI) near2 (YASHIMA)).INV. and @pd> "20090728"	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/10/29 12:52
L12	0	((NAOMI) near2 (YONEMURA)).INV. and @pd> "20090728"	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/10/29 12:52
L13	14	denki.as. and (circuit with (board or substrate)).clm. and @pd> "20090728"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2009/10/29 12:52

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	((hybrid or mix\$3) near2 (circuit or device) and (capacitor or capacitance) with (insulat\$3 or passivat\$3 or dielectric)).clm. and @pd> "20090728"	USPAT; UPAD	ADJ	ON	2009/10/29 12:41
L2	40	(metal near5 (board or plate or base) with portion and pad and (insulat\$3 or dielectric or passivat\$3) and (power and control or first and second) near2 (chip or die or device or component)).clm.	USPAT; UPAD	ADJ	ON	2009/10/29 12:44

10/ 29/ 2009 12:53:32 PM

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